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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Morio Nakao

Docket Number: TI-35373

Serial No.: 10/631,083

Art Unit: 2822

Filed: 07/31/2003

Examiner: David E. Graybill

For: **HIGH RELIABILITY CHIP SCALE PACKAGE**

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NAME OF INVENTOR(S): Morio Nakao		RECEIPT DATE & SERIAL NO.: Serial No.: 10/631,083	
TITLE OF INVENTION: High Reliability Chip Scale Package			
TI FILE NO.: TI-35373	DEPOSIT ACCT. NO.: 20-0668		
FAXED: 12/20/2004			
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION**

Appl. No. : 10/631,083
Applicant : Morio Nakao
Filed : July 31, 2003
TC/A.U. : 2822
Examiner : David E. Graybill

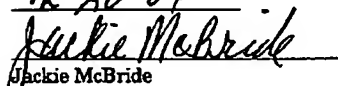
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Docket No. : TI-35373
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Jackie McBride

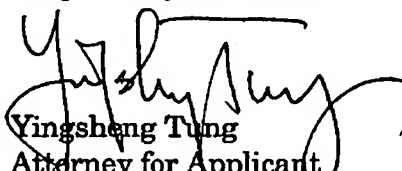
ELECTION

Sir:

In response to the Examiner's restriction requirement mailed November 26, 2004, applicant hereby elects to pursue Group III, claims 10-17, for examination purposes.

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Respectfully submitted,


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